

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"20040239379"	US-PGPUB; USPAT	OR	OFF	2007/11/29 11:19
L2	694	current adj (data or signal) with amplif\$4 adj circuit	US-PGPUB; USPAT	OR	ON	2007/11/29 11:20
L3	158	2 same transistor	US-PGPUB; USPAT	OR	ON	2007/11/29 11:32
L4	123	3 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/11/29 11:39
L5	2	4 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 11:41
L6	30	4 and "327"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 11:21
L7	24253	transistor same series same parallel same connect\$4	US-PGPUB; USPAT	OR	ON	2007/11/29 11:40
L8	56	2 and 7	US-PGPUB; USPAT	OR	ON	2007/11/29 11:33
L9	535	7 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 11:39
L10	373	9 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/11/29 11:41
L11	183	10 and amplif\$4	US-PGPUB; USPAT	OR	ON	2007/11/29 11:39
L12	12482	transistor with series with connect\$3 same parallel with connect\$4 with transistor	US-PGPUB; USPAT	OR	ON	2007/11/29 12:26
L13	72359	12and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 11:41
L14	293	12 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 12:10
L15	197	14 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/11/29 12:11
L16	1	"20030142046".PN.	US-PGPUB	OR	OFF	2007/11/29 12:06
L17	1	"20030058687".PN.	US-PGPUB	OR	OFF	2007/11/29 12:06
L18	1	"20020047817".PN.	US-PGPUB	OR	OFF	2007/11/29 12:08
L19	1	"6611107".PN.	USPAT; USOCR	OR	OFF	2007/11/29 12:08
L20	1	"6611107".PN.	USPAT; USOCR	OR	OFF	2007/11/29 12:09
L21	1	"6909242".pn.	USPAT	OR	OFF	2007/11/29 12:10
L22	2234	12 and "327"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 12:26

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L23	109	12 and "327"/50-57.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 12:11
L24	103	23 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/11/29 12:29
L25	3478	transistor with series with connect\$3 same parallel with connect\$4 with transistor	EPO; JPO; DERWENT	OR	ON	2007/11/29 12:14
L26	485	25 and amplif\$3	EPO; JPO; DERWENT	OR	ON	2007/11/29 12:14
L27	348	transistor with series with connect\$3 with input\$3 same parallel with connect\$4 with transistor with output\$3	EPO; JPO; DERWENT	OR	ON	2007/11/29 12:14
L28	96	27 and amplif\$3	EPO; JPO; DERWENT	OR	ON	2007/11/29 12:19
L29	4	27 and display	EPO; JPO; DERWENT	OR	ON	2007/11/29 12:21
L30	738	inukai-k\$.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/29 12:21
L31	37	30 and amplifier	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/29 12:22
L32	828	12 and "315"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 12:26
L33	1281	transistor with series with connect\$3 with input\$3 same parallel with connect\$4 with transistor with output\$3	US-PGPUB; USPAT	OR	ON	2007/11/29 13:59
L34	140	33 and display	US-PGPUB; USPAT	OR	ON	2007/11/29 12:29
L35	112	34 and @ad<="20031223"	US-PGPUB; USPAT	OR	ON	2007/11/29 12:29
L36	8	35 and "315"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 12:33
L37	1	"20040150350"	US-PGPUB; USPAT	OR	ON	2007/11/29 13:43
L38	1	"6798148".pn.	US-PGPUB; USPAT	OR	ON	2007/11/29 13:58

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L39	6	"1102234"	EPO; JPO; DERWENT	OR	OFF	2007/11/29 13:49
L40	7	"1103946"	EPO; JPO; DERWENT	OR	OFF	2007/11/29 13:50
L41	2	"2001147659"	EPO; JPO; DERWENT	OR	OFF	2007/11/29 13:50
L42	2	"2001343933"	EPO; JPO; DERWENT	OR	OFF	2007/11/29 13:50
L43	881	(transistor with series with connect\$3 with input\$3 and parallel with connect\$4 with transistor with output\$3).clm.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:00
L44	37	(display and transistor with series with connect\$3 with input\$3 and parallel with connect\$4 with transistor with output\$3).clm.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:01
L45	271	(transistor with series with connect\$3 with input\$3 with current and parallel with connect\$4 with transistor with output\$3 with current).clm.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:54
L46	4	45 and "345"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:05
L47	12	45 and "315"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:06
L48	0	45 and "3327"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:06
L49	85	45 and "327"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:06
L50	33	(driving and transistor with series with connect\$3 with input\$3 with current and parallel with connect\$4 with transistor with output\$3 with current).clm.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:54
L51	106	(amplif\$3 and transistor with series with connect\$3 with input\$3 with current and parallel with connect\$4 with transistor with output\$3 with current).clm.	US-PGPUB; USPAT	OR	ON	2007/11/29 14:53
L52	15	(amplif\$3 and transistor with series with connect\$3 with input\$3 with current and parallel with connect\$4 with transistor with output\$3 with current).clm.	US-PGPUB	OR	ON	2007/11/29 14:53
L53	10	(driving and transistor with series with connect\$3 with input\$3 with current and parallel with connect\$4 with transistor with output\$3 with current).clm.	US-PGPUB	OR	ON	2007/11/29 14:54

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L54	41	(transistor with series with connect\$3 with input\$3 with current and parallel with connect\$4 with transistor with output\$3 with current).clm.	US-PGPUB	OR	ON	2007/11/29 14:54
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